

FIG.3

301 LOGIC CIRCUIT

MASK

303 BIT MASK CIRCUIT

FIG.7

151: CELL B1

FIG.8

152: CELL B2

FIG.9

153: CELL B3

FIG.13

151: CELL B1

FIG.14

152: CELL B2

FIG.15

153: CELL B3

FIG.16

154: CELL B4

FIG.18

2F04079-PCT

CONNECTED TO CARRY NETWORK

301 LOGIC CIRCUIT

MASK

303 BIT MASK CIRCUIT

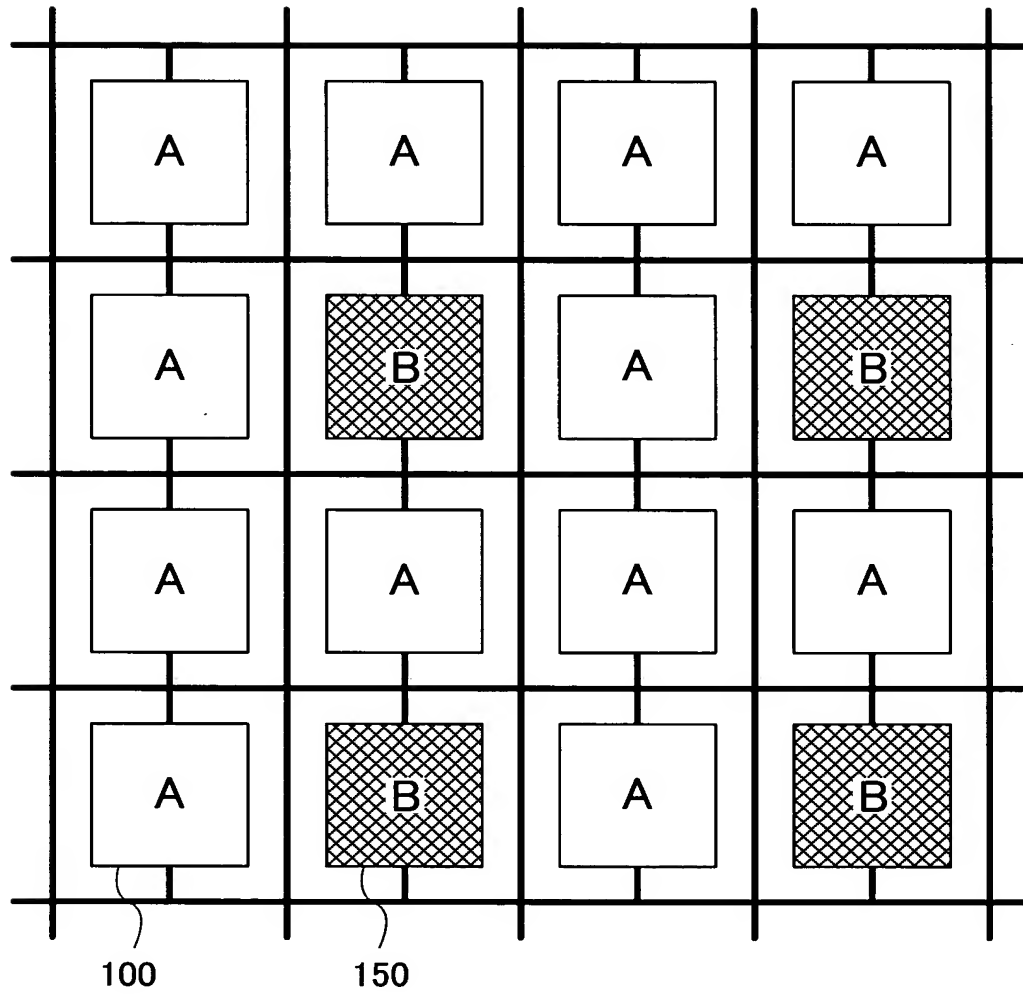


FIG.1

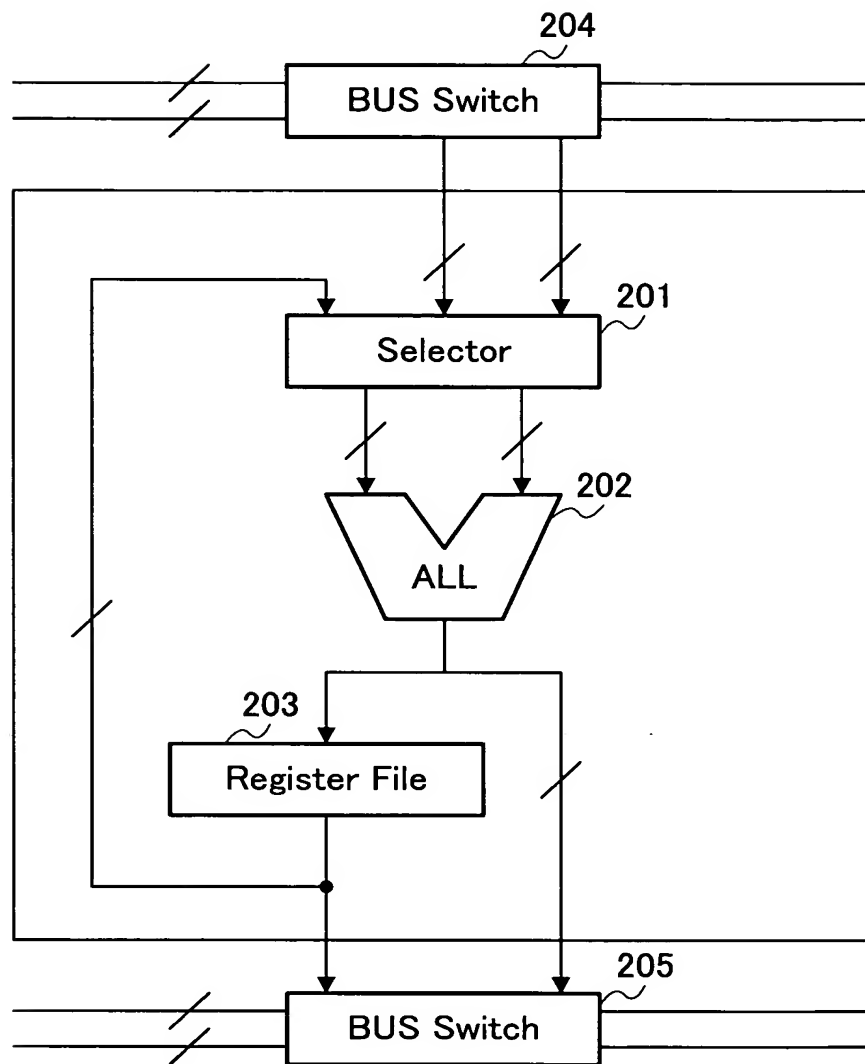


FIG.2

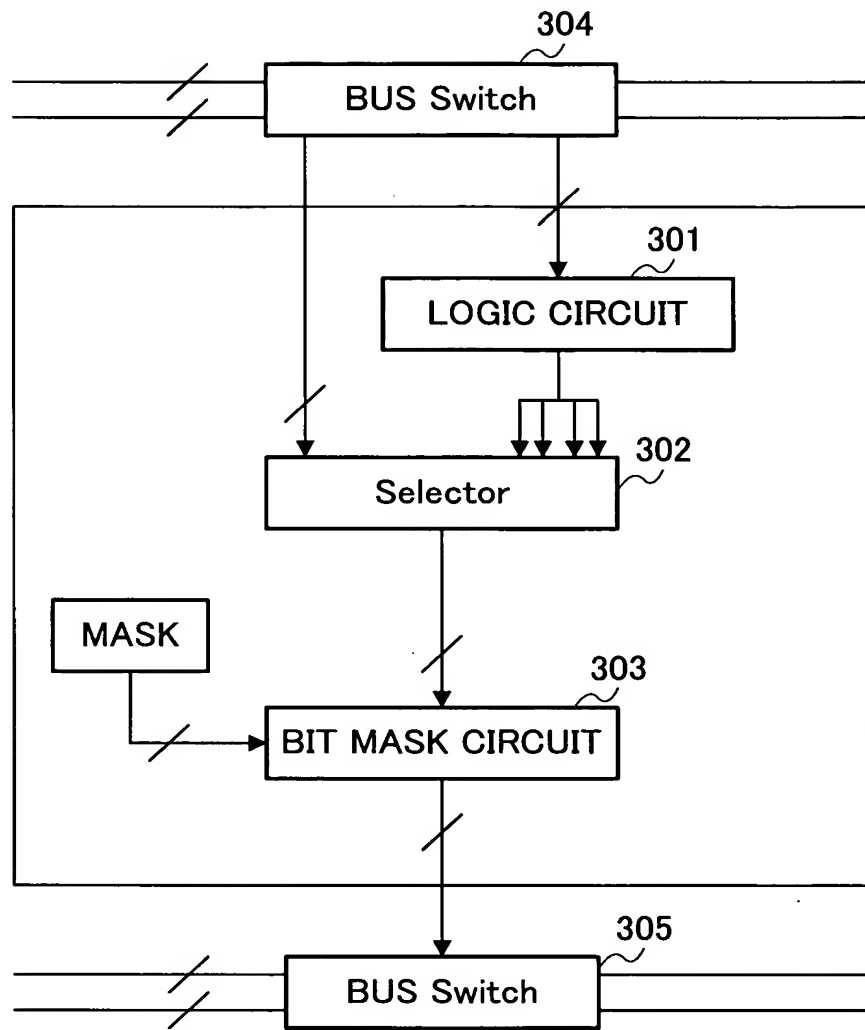


FIG.3

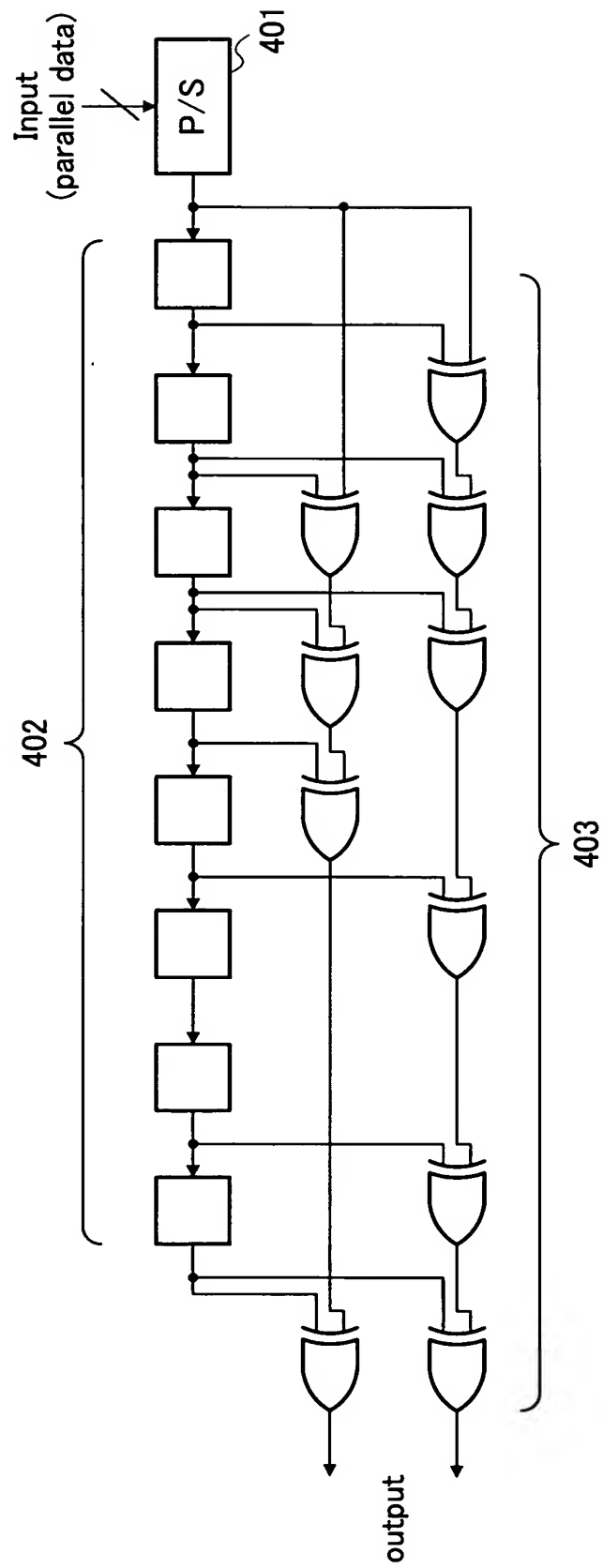


FIG.4

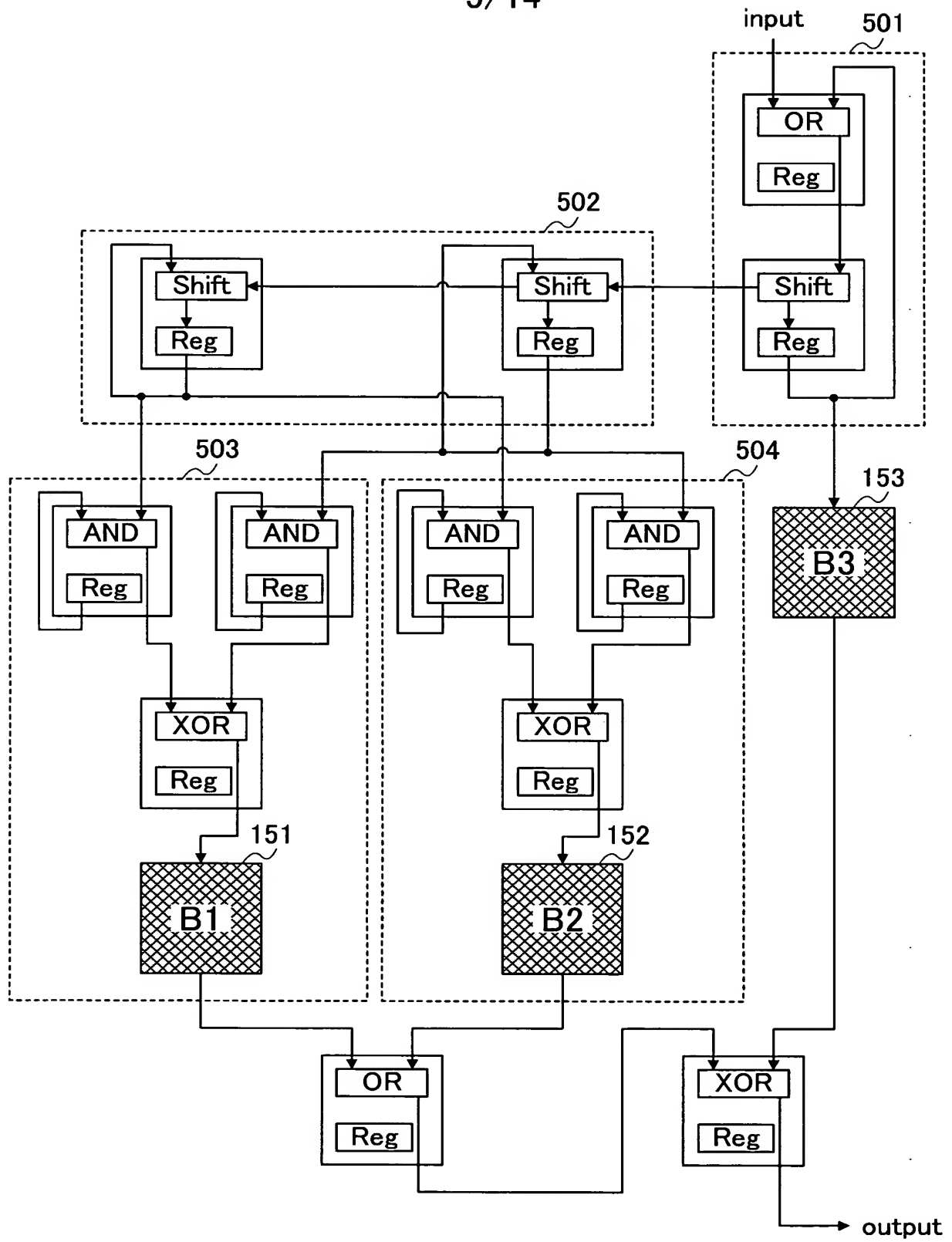


FIG.5

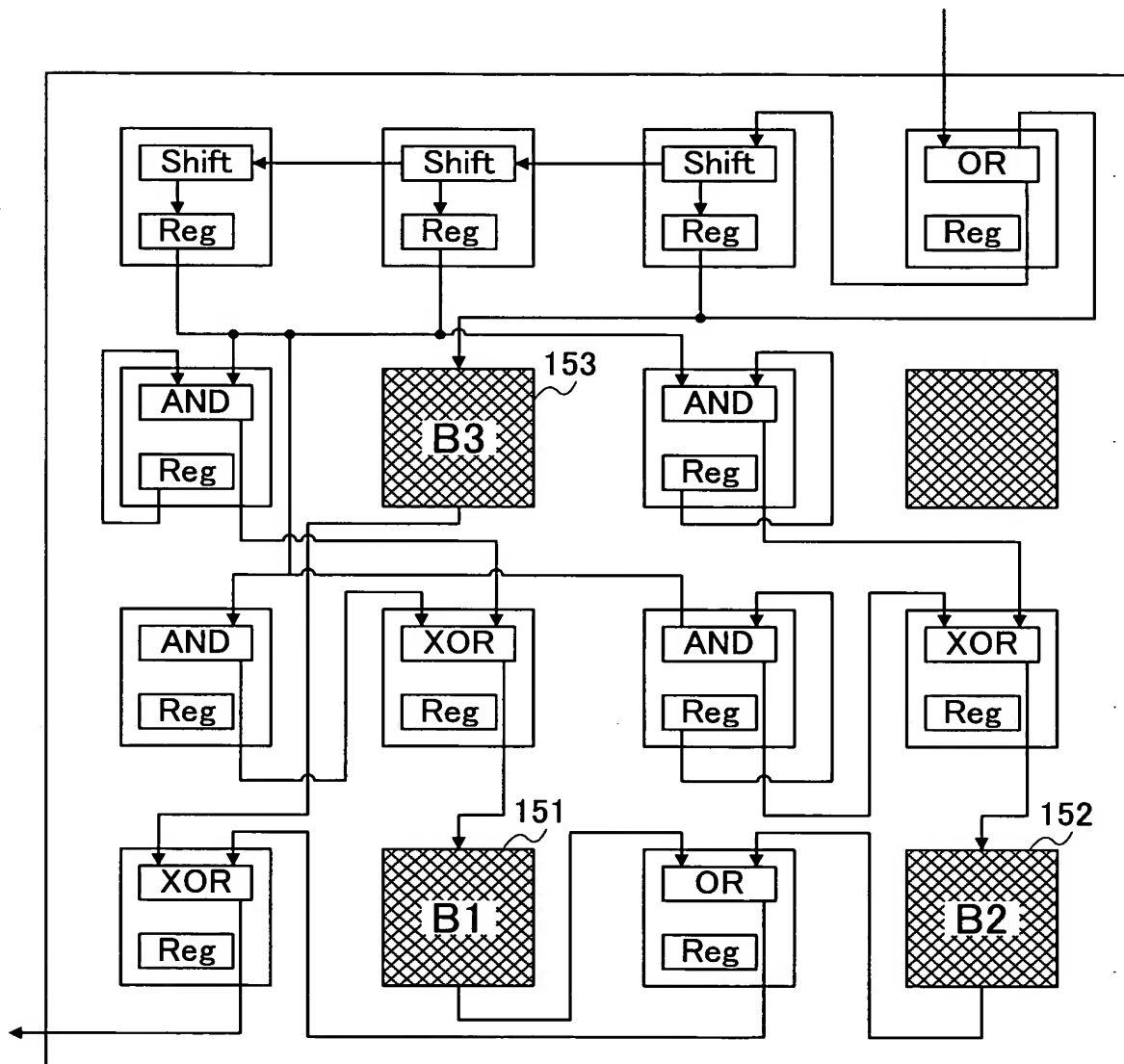


FIG. 6



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151: CELL B1

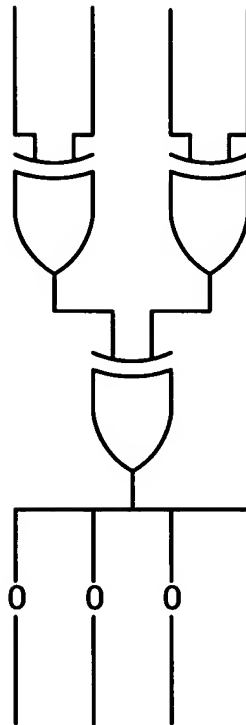


FIG.7

152: CELL B2

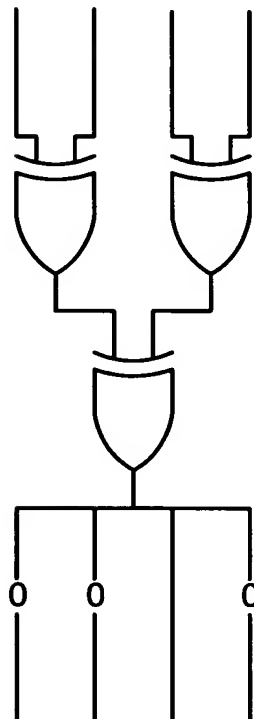


FIG.8

153: CELL B3

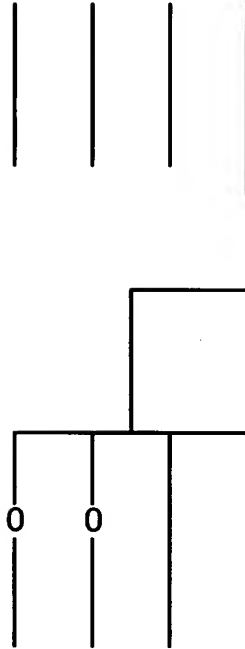


FIG.9

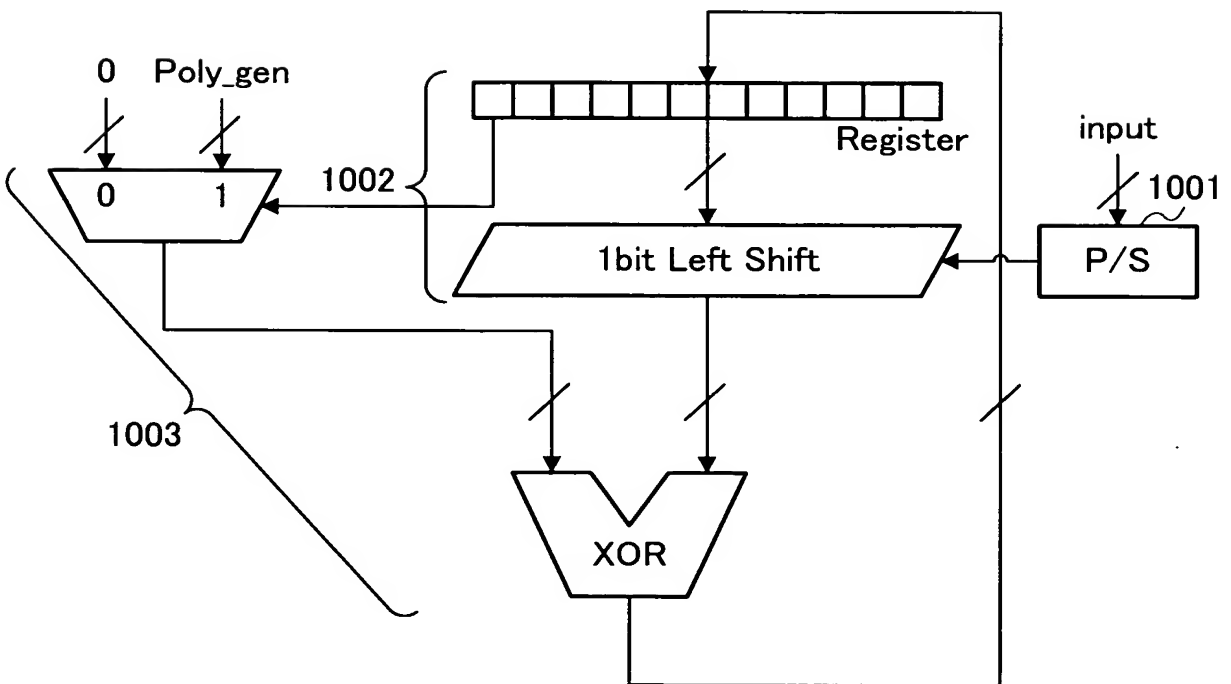


FIG.10

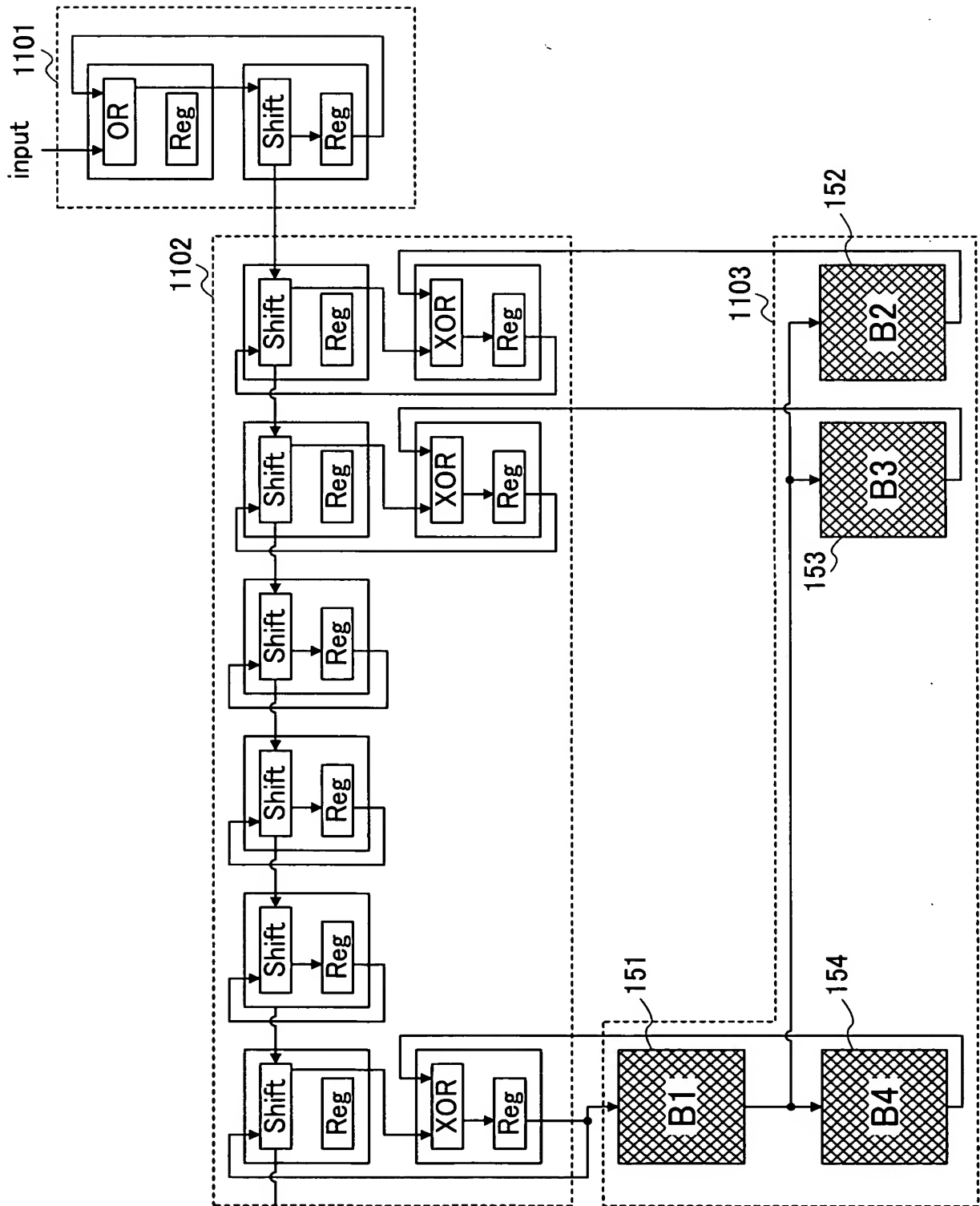


FIG.11

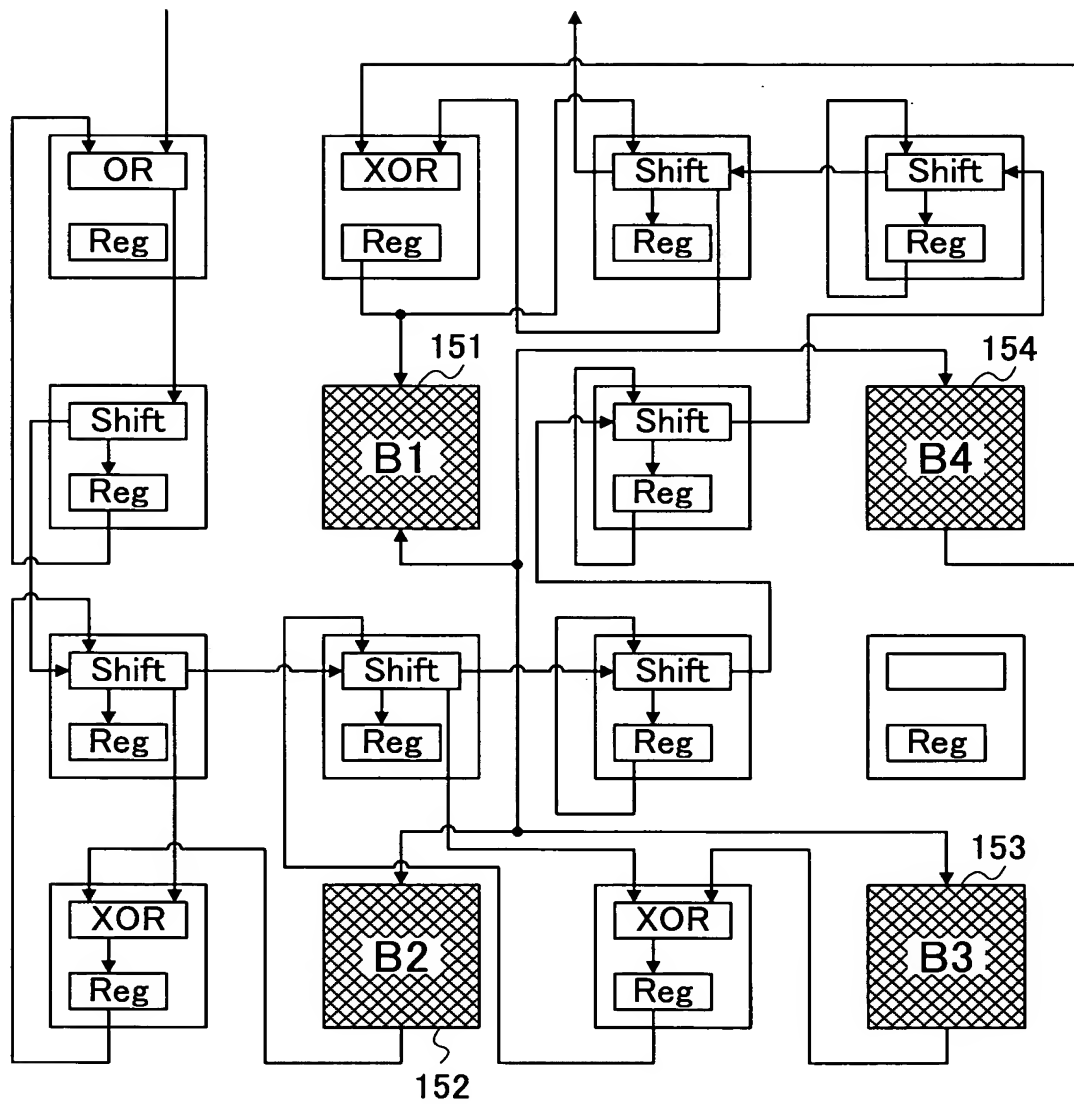


FIG.12

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151: CELL B1

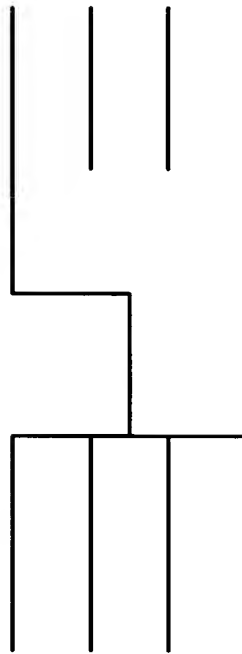


FIG.13

152: CELL B2

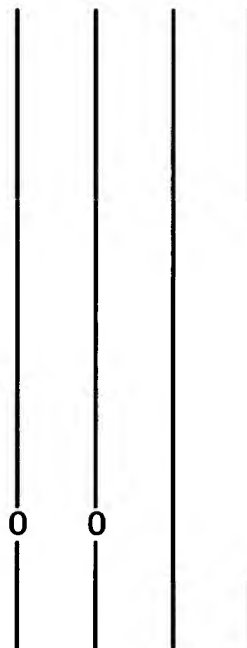


FIG.14

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153: CELL B3

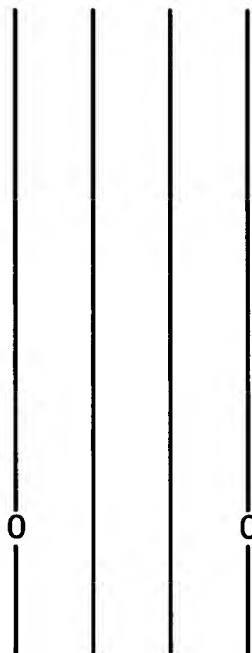


FIG.15

154: CELL B4

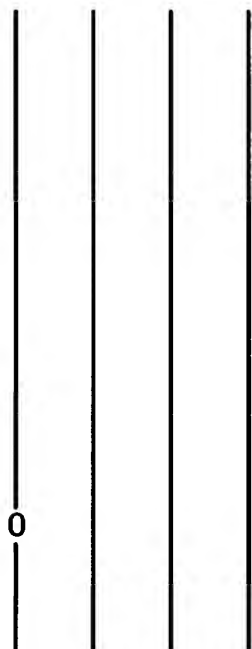


FIG.16

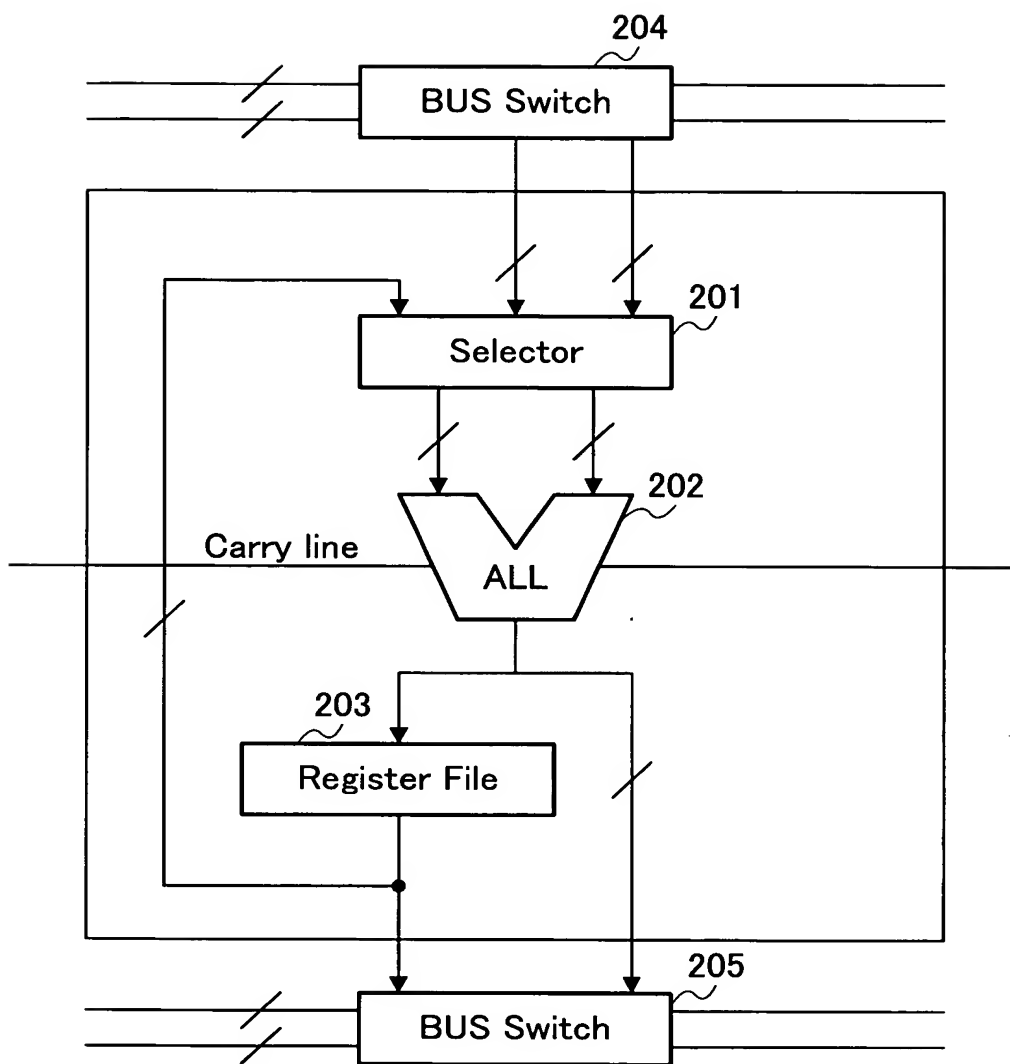


FIG.17

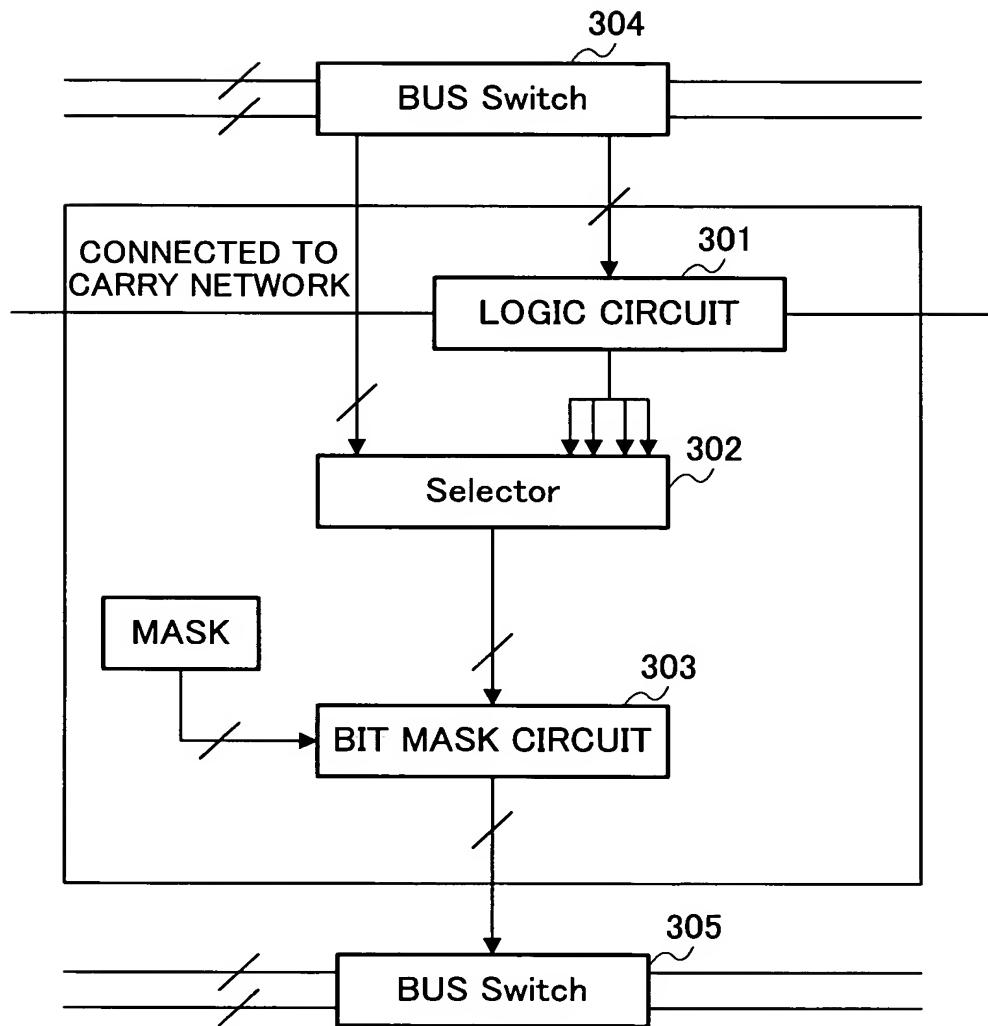


FIG.18